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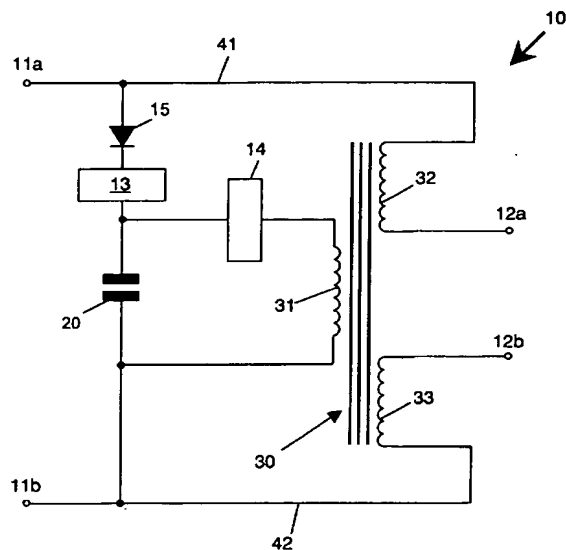
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(54) Title: **IGNITING PULSE BOOSTER CIRCUIT**



(57) Abstract: A pulse booster circuit (10) comprises a first pulse transfer path (41) and a second pulse transfer path (42) extending between input terminals (11a; 11b) and output terminals (12a; 12b). A series arrangement of a capacitor (20) and a first breakdown switch (13) is connected between said two input terminals (11a; 11b). A series arrangement of a second breakdown switch (14) and a primary winding (31) of a transformer (30) is connected in parallel to said capacitor (20). A first output winding (32) of said transformer (30) is incorporated in said first pulse transfer path (41), while a second output winding (33) of said transformer (30) is incorporated in said second pulse transfer path (42).

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